

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,299,355 B2
APPLICATION NO. : 10/042019
DATED : November 20, 2007
INVENTOR(S) : Zheng Qi

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Claim 1, Column 12, Line 51, please delete "theoutput" and replace with --the output--.

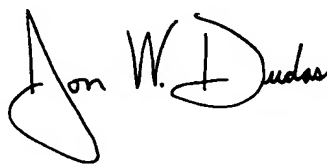
In Claim 2, Column 12, Line 56, please delete "thee 32-bit CSAs" and replace with --three 32-bit CSAs--.

In Claim 4, Column 12, Line 62, please delete "wherein combined adder tree includes add 5tol and add 4tol adders." and replace with --wherein the combined adder tree includes add5tol and add4tol adders.--.

Column 3, Lines 56-58, please delete "As noted above, of the two algorithms supported by the IPSec protocol, HMAC-SHA1-96 is about twenty-five percent slower".

Signed and Sealed this

Twenty-ninth Day of April, 2008

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS
Director of the United States Patent and Trademark Office